DIO1642
Three Port, 4GHz, High-Speed MIPI Switch

Features

- Supply voltage:1.65V to 5.5V
- Quiescent Current: 17uA
- Low On Resistance: 10Ω
- -3dB Bandwidth: 4GHz
- Package: QFN3.4*2.5-24

Applications

- Dual Camera for Cell Phones
- Dual LCD Monitor, Digital Camera Displays

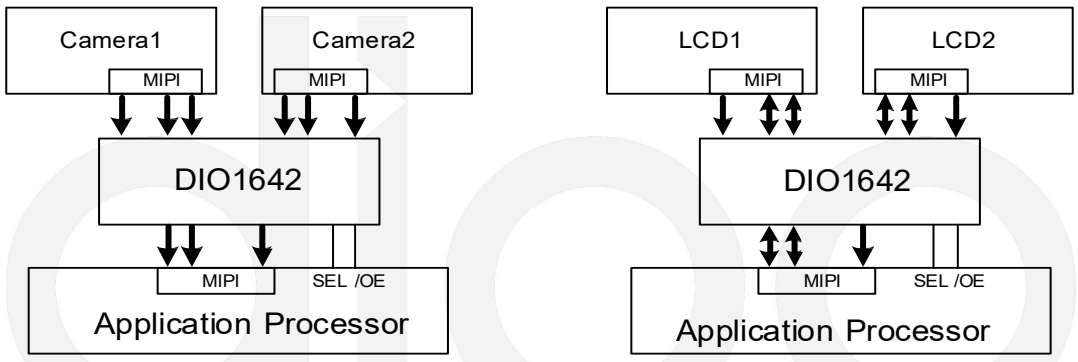
Descriptions

The DIO1642 is a high-speed analog switch. The pin out is designed to ease differential signal layout and is configured as triple-pole, double-throw switch (TPDT). The DIO1642 is optimized for switching between two MIPI devices, such as cameras or LCD displays and on-board MAP.

The DIO1642 is compatible with the requirements of MIPI. The low-capacitance design allows the device to switch signals that exceed 4GHz in frequency. Superior channel-to-channel crosstalk immunity minimizes the interference and allows the transmission of high-speed differential signals and single-ended signals, as described by the MIPI specification.

The DIO1642 is available in QFN3.4*2.5-24 package. Standard Products are Pb-free and halogen-free.

Typical Application



Ordering Information

Order Part Number	Top Marking		T_A	Package	
DIO1642EN24	AFD2	Green	-40 to 85℃	QFN3.4*2.5-24	Tape & Reel, 5000

Pin Assignments

QFN3.4*2.5-24

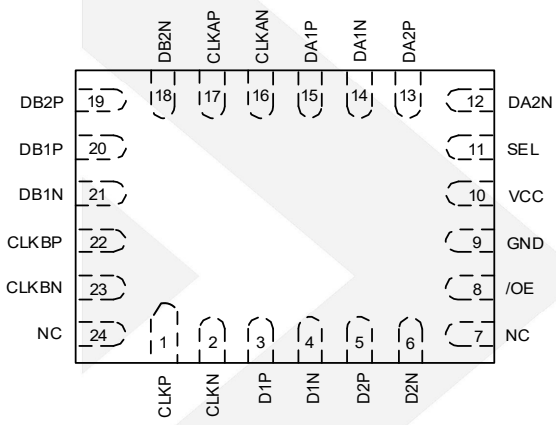


Figure 1. Top View

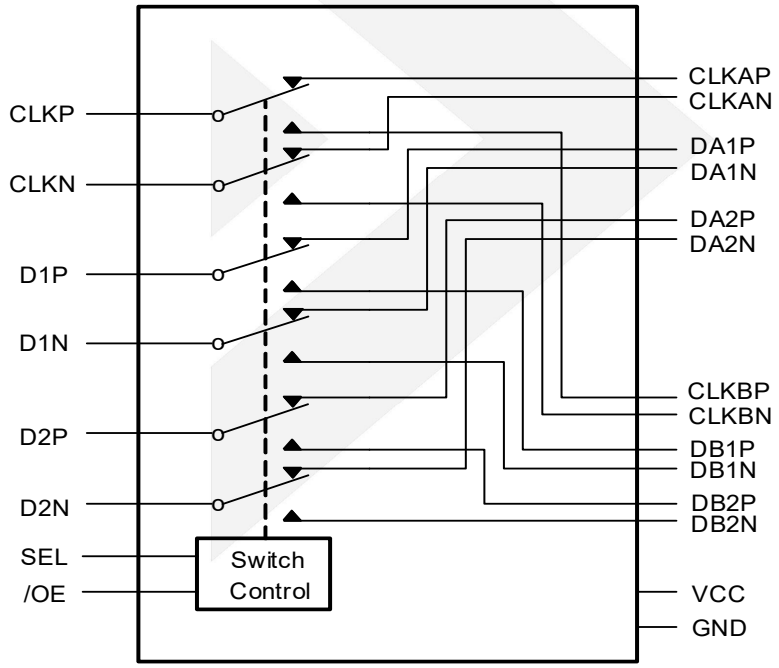
Pin Description

Pin Name	Description
CLKP/N	Clock Paths (CLKP, CLKN)
D1P/N	Data Paths 1 (D1P, D1N)
D2P/N	Data Paths 2 (D2P, D2N)
/OE	Output Enable (Active Low)
GND	Ground
VCC	Power Supply
SEL	Select (0=A, 1=B)
DA2P/N	Data Paths (DA2P, DA2N)
DA1P/N	Data Paths (DA1P, DA2N)
CLKAP/N	Clock Paths (CLKAP, CLKAN)
DB2P/N	Data Paths (DB2P, DB2N)
DB1P/N	Data Paths (DB1P, DB2N)
CLKBP/N	Clock Paths (CLKBP, CLKBN)
NC	No Connect

Truth Table

SEL	/OE	Function
X	H	Bus switch disconnected
L	L	D1,D2,CLK = DA1,DA2,CLKA
H	L	D1,D2,CLK = DB1,DB2,CLKB

Function Diagram



Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Rating	Unit
VCC	Supply voltage range	-0.5 to 6.5	V
V_{DATA}	Data input/output voltage range	-0.5 to 6.5	V
V_{SEL}	Select input voltage range	-0.5 to 6.5	V
I_{OUT}	Continues output current	±50	mA
T_J	Junction temperature range	150	℃
T_L	Lead temperature range	260	℃
T_{STG}	Storage temperature range	-65 to 150	℃
$R_{\theta JA}$	Thermal resistance	250	℃/W
ESD Human Body Model (HBM)	I/O to VCC, I/O to GND	±5000	V

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

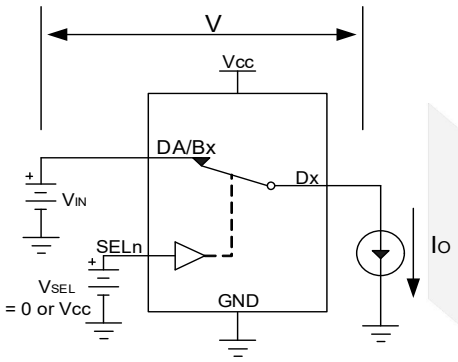
Symbol	Parameter	Rating	Unit
VCC	Supply voltage range	1.65 to 5.5	V
V_{DATA}	Data input/output voltage range	0 to VCC	V
V_{SEL}	Select input voltage range	0 to VCC	V
$V_{/OE}$	Enable control input voltage range	0 to VCC	V
T_A	Operating temperature range	-40 to 85	℃

Electrical Characteristics

T_A=25°C, V_{CC}=2.5V, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{IH}	Select and /OE logic high level	VCC=3.6 to 4.5V	1.5			V
		VCC=2.3 to 3.6V	1.3			V
V_{IL}	Select and /OE logic low level	VCC=3.6 to 4.5V			0.6	V
		VCC=2.3 to 3.6V			0.4	V
I_{CC}	Supply quiescent current	I_{OUT}=0, V_{SEL} >1.5V or V_{SEL}< 0.7V		17	35	uA
I_{SEL}	Select input leakage current	V_{SEL}=VCC			±1.0	uA
I_{OFF}	Off state switch leakage current				±1.0	uA
R_{ON}	On-Resistance	VCC=3.0V, V_{DATA}=0 to 0.4V, I_{OUT}=8mA		10	13	Ω
Δ R_{ON}	On-Resistance match	VCC=3.0V, V_{DATA}=0 to 0.4V, I_{OUT}=8mA		0.1		Ω
$R_{FLAT(ON)}$	On-Resistance flatness	VCC=3.0V, V_{DATA}=0 to 1.0V, I_{OUT}=8mA		1		Ω
T_{PD}	Propagation delay time	C_L=5pF, R_L=50Ω		0.2		ns
T_{ON}	Select input to switch on time	C_L=10pF, R_L=50Ω		90	120	ns
T_{OFF}	Select input to switch off time	C_L=10pF, R_L=50Ω		40	80	ns
T_{BBM}	Break-Before-Make time	Generated by design		50		ns
BW	-3dB Bandwidth	R_L=50Ω, C_L=0pF	3	4		GHz
OIRR	Off isolation	R_L=50Ω, F=100MHz		-40		dB
Xtalk	Crosstalk	R_L=50Ω, F=100MHz		-44		dB
C_{IN}	Select pin input capacitance	VCC=0V		6		pF
C_{OFF}	D1n, D2n,Dn Off capacitance	VCC=3.3V, /OE=3.3V		5		pF
C_{ON}	D1n, D2n,Dn On capacitance	VCC=3.3V, /OE=0V		4		pF

Application Information



$R_{ON}=V/I_O$

Figure 2. ON Resistance

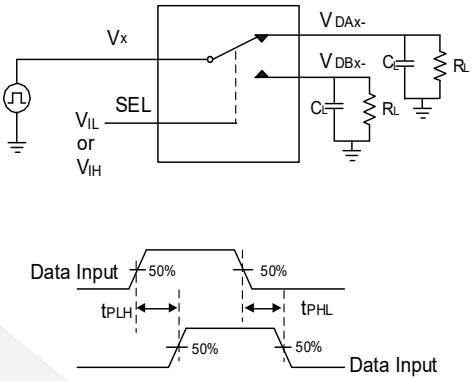
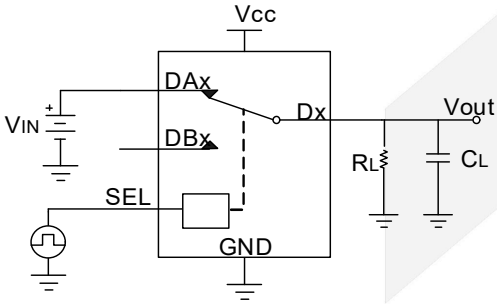


Figure 3. Propagation delay time



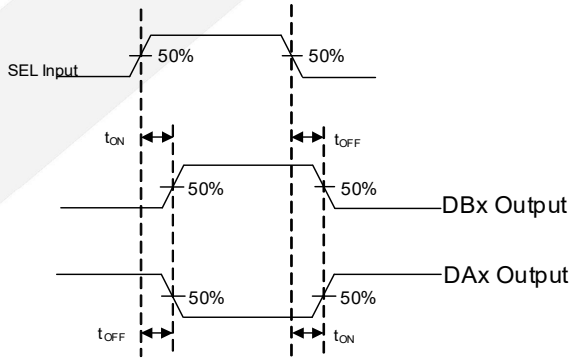


Figure 4. Select input to switch on/off time

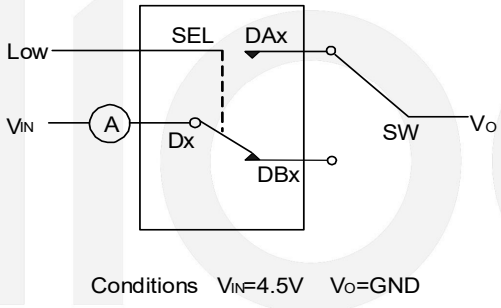


Figure 5. Off state switch leakage current

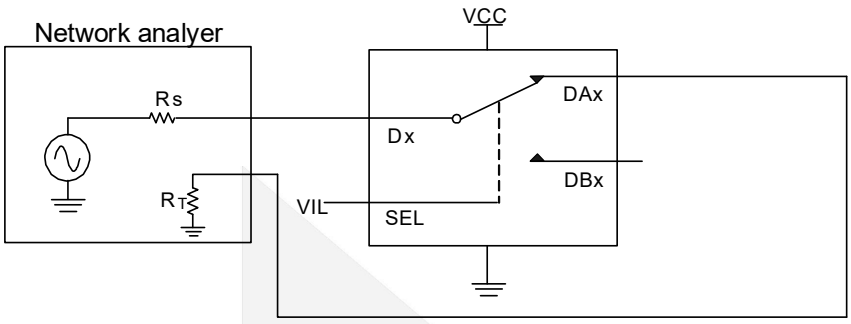


Figure 6. Bandwidth (BW)

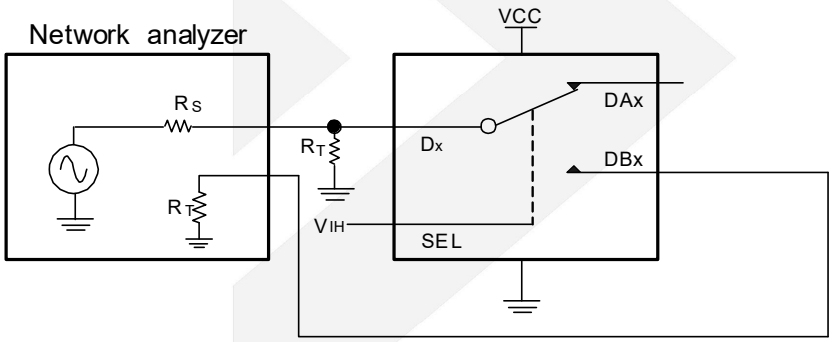


Figure 7. Off isolation (OIRR)

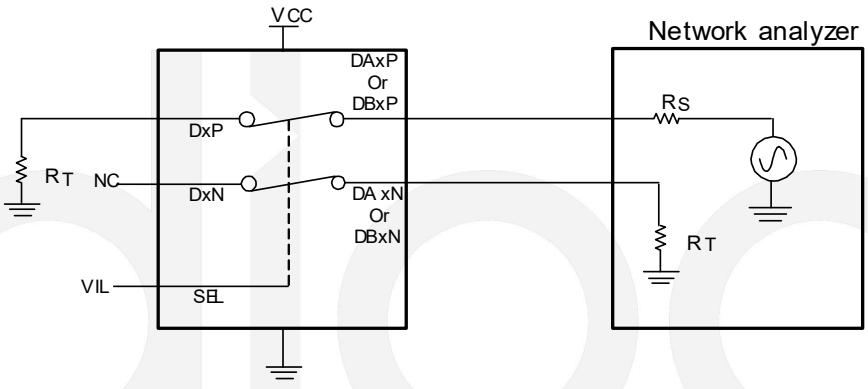


Figure 8. Crosstalk (Xtalk)

Typical Performance Characteristics

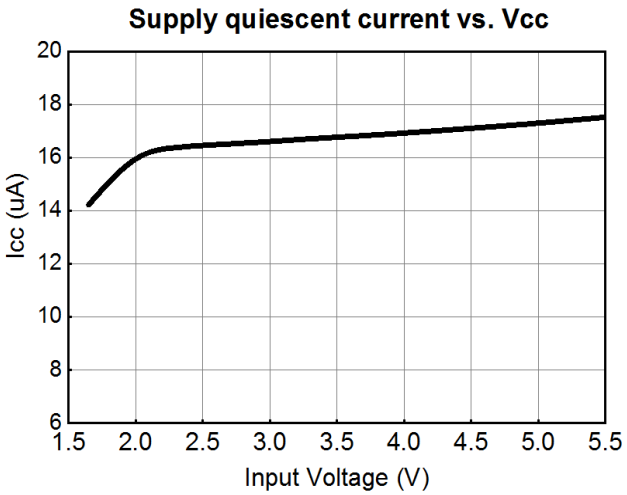


Figure 9 Supply quiescent current Vs. Vcc

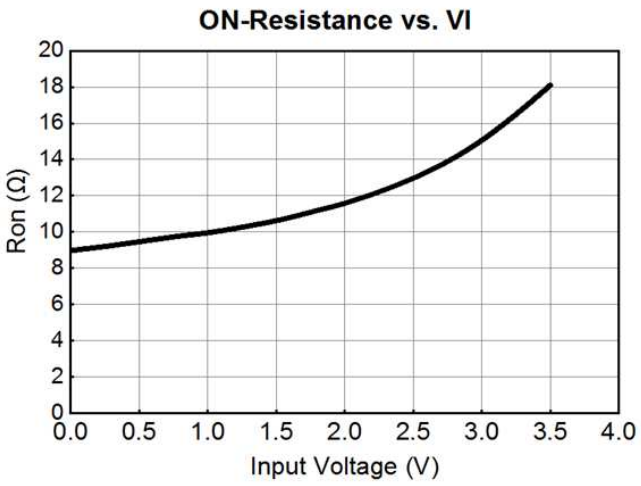


Figure 10 Ron Vs. VI

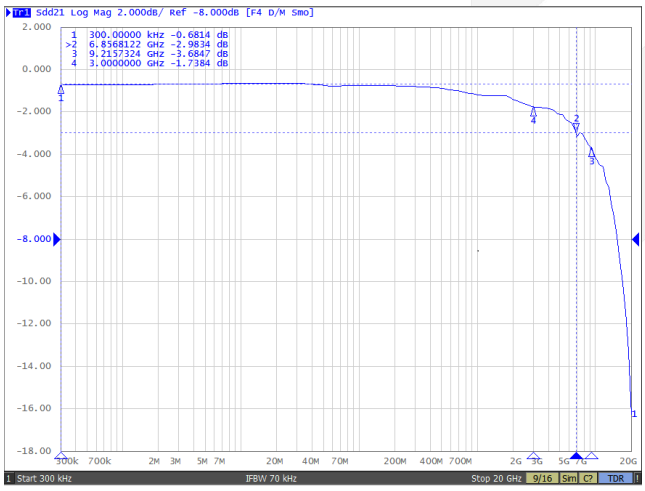


Figure 11 Bandwidth Vs. Frequency

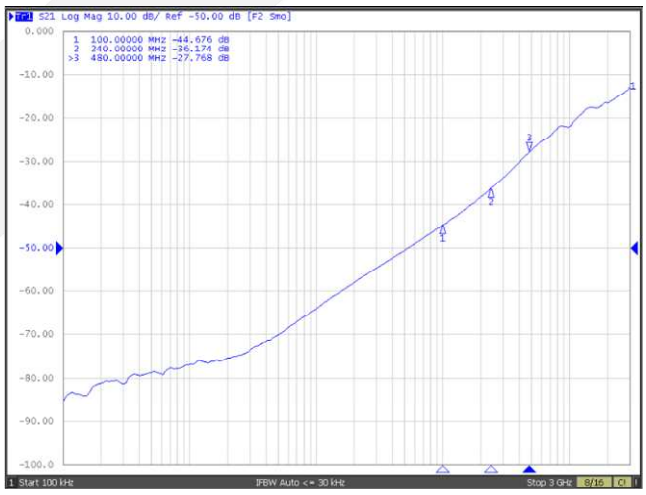


Figure 12 Cross Talk Vs. Frequency

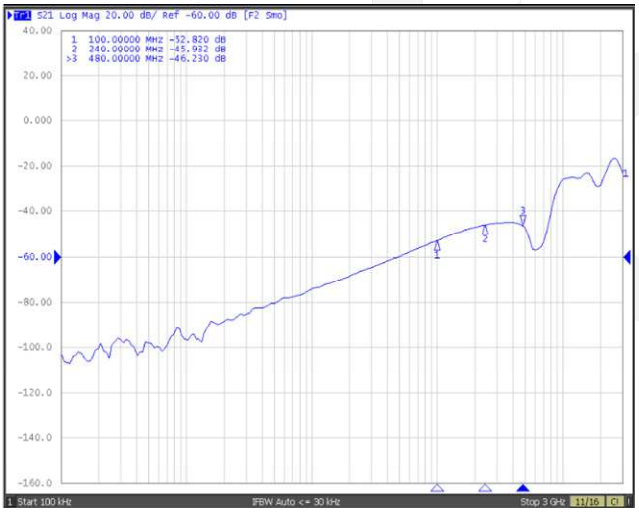


Figure 13 Off Isolation Vs. Frequency

Three Port, 4GHz, High-Speed MIPI Switch

CONTACT US

Dioo is a professional design and sales corporation for high-quality and performance analog semiconductors. The company focuses on industry markets, such as, cell phone, handheld products, laptop, and medical equipment and so on. Dioo's product families include analog signal processing and amplifying, LED drivers and charger IC. Go to http://www.dioo.com for a complete list of Dioo product families.

For additional product information, or full datasheet, please contact with our Sales Department or Representatives.