DIO7910
300mA, Ultra-Low-Noise, Low-IQ LDO

Features

- Operating Input Voltage Range: 1.6V to 5.5 V
- Output Voltage Range: 0.8 V to 3.3 V
- Output current: 300mA
- Ultra-Low Quiescent Current : Typ. 25μA
- Dropout voltage : 140mV @ I_{OUT}=300mA
- PSRR: 75dB @ 1kHz, I_{OUT} = 20mA
- Output Voltage Tolerance: ±1%
- Stable with Ceramic Capacitors 1 μF
- Thermal-Overload Protection
- Short-Circuit Protection
- Quick Output Discharge
  DIO7910A: available
  DIO7910B: not available
- Available in Small DFN1*1-4 and SOT23-5 Packages
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- MP3/MP4 Players
- Cellphones, radiophone, digital cameras
- Bluetooth, wireless handsets
- Others portable electronics device

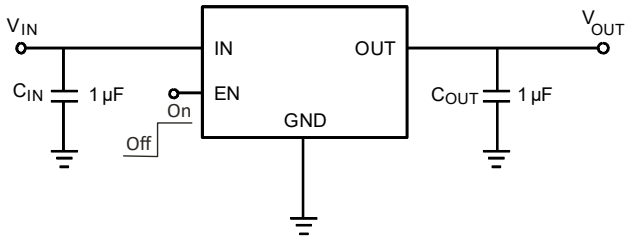
Descriptions

The DIO7910 series is a high accuracy, low noise, high speed, high PSRR, low dropout CMOS Linear regulator with high ripple rejection. The devices offer a new level of cost effective performance in cellular phones, laptop and notebook computers, and other portable devices.

The DIO7910 has the fold-back maximum output current which depends on the output voltage. So the current limit functions both as a short circuit protection and as an output current limiter.

The device is available in DFN1*1-4 and SOT23-5 packages.

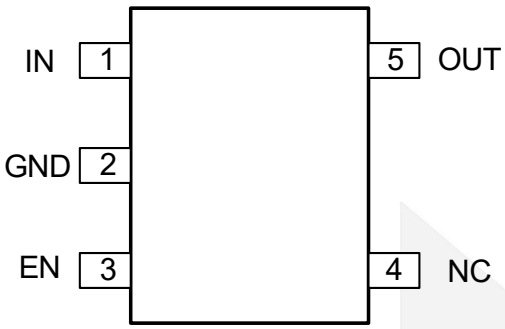
Typical Applications



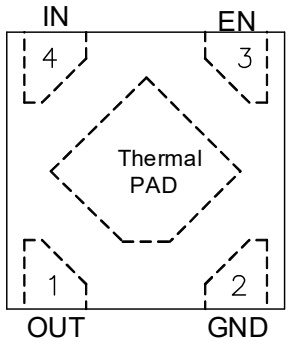
Ordering Information

Order Part Number	Top Marking		T_A	Package	
DIO7910A08ST5	KADYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A10ST5	KAEYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A11ST5	KACYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A12ST5	KAFYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A15ST5	KAGYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A18ST5	KAHYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A25ST5	KAJYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A28ST5	KAKYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A30ST5	KAMYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A33ST5	KANYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910A08EN4	YWKD	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A10EN4	YWKE	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A11EN4	YWKC	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A12EN4	YWKF	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A15EN4	YWKG	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A18EN4	YWKH	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A25EN4	YWKJ	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A28EN4	YWKK	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A30EN4	YWKM	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910A33EN4	YWKN	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B08ST5	KBDYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B10ST5	KBEYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B11ST5	KBCYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B12ST5	KBFYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B15ST5	KBGYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B18ST5	KBHYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B25ST5	KBJYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B28ST5	KBKYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B30ST5	KBMYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B33ST5	KBNYW	Green	-40 to 85°C	SOT23-5	Tape & Reel,3000
DIO7910B08EN4	YWVD	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B10EN4	YWVE	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B11EN4	YWVC	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B12EN4	YWVF	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B15EN4	YWVG	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B18EN4	YWVH	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B25EN4	YWVJ	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B28EN4	YWVK	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B30EN4	YWVM	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000
DIO7910B33EN4	YWVN	Green	-40 to 85°C	DFN1*1-4	Tape & Reel,10000

Pin Assignments



SOT23-5



DFN1*1-4

Figure 1 Pin Assignment (Top View)

Pin Definitions

Pin Name	Description
OUT	Output Voltage Pin.
EN	Enable Pin. This pin has an internal pull-down resistor. A logic low reduces the supply current to less than 1μA. Connect to logic “High” for normal operation.
GND	Power Supply Ground.
IN	Input Voltage Pin.
NC	No connection

Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maxim rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit
Input Voltage V_{IN}		-0.3 to 6.5	V
Output Voltage V_{OUT}		-0.3 to V_{IN}	V
Chip Enable Input V_{EN}		-0.3 to V_{IN}	V
Output Current I_{OUT}		300	mA
Lead Temperature Range		260	°C
Operating Junction Temperature $T_{J(MAX)}$		150	°C
Storage Temperature T_{STG}		-55 to 150	°C
MSL		Level-3	
Thermal Resistance	DFN1*1-4	250	°C/W
	SOT23-5	250	
ESD	HBM	2000	V
	MM	300	

Recommend Operating Ratings

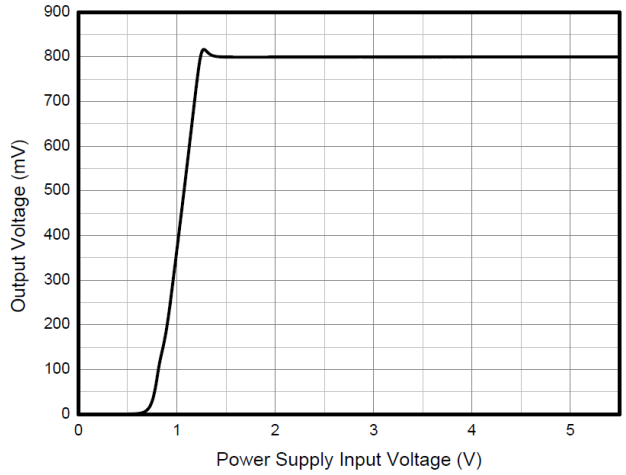
Parameter		Rating	Unit
Operating Supply voltage		1.6～5.5	V
Operating Temperature Range		-40～85	℃
Thermal Resistance, $R_{\theta JA}$	SOT23-5	250	℃/W
	DFN1*1-4	250	

Electrical Characteristics

V_{IN} = Vout+1V, I_{OUT}=1mA, C_{IN}=C_{OUT}=1.0μF, Full = -40℃ to +85℃, Typical values are at T_A = 25°C, unless otherwise specified.

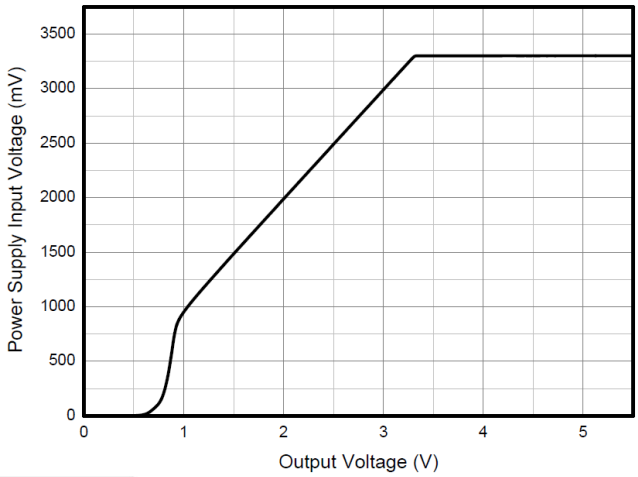
Symbol	Parameter	Condition		Min	Typ	Max	Unit
V_{IN}	Input Voltage			1.6		5.5	V
V_{OUT}	Output Voltage			0.8		3.3	V
	Output Accuracy	V_{OUT} < 2V		-20	V_{OUT}	+20	mV
		V_{OUT}≥2V		-1%× V_{OUT}	V_{OUT}	1%× V_{OUT}	V
I_{LIM}	Output Current Limit	V_{OUT} = 90% $V_{OUT(NOM)}$			480		mA
V_{DROP}	Dropout Voltage	V_{OUT}=2.8V, I_{OUT}=300mA			140		mV
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation	$V_{OUT(NOM)}$ + 1.0V ≤ V_{IN} ≤ 5.5V			0.02		%/V
$\triangle V_{OUT}$	Load Regulation	I_{OUT} = 1mA to 300mA				30	mV
I_Q	Quiescent Current	No load			25		μA
I_{SC}	Short Circuit Current	V_{OUT} = 0V			220		mA
I_{SHDN}	Shut-down Current	V_{EN} = 0V, V_{IN} = 5.5V			0.1		μA
PSRR	Power Supply Rejection Rate	I_{OUT} = 20mA	f=100Hz		80		dB
			f=1kHz		75		dB
			f=10kHz		70		dB
			f=100kHz		60		dB
			f=1MHz		45		dB
V_{IH}	EN Pin Threshold Voltage	EN logic high voltage		1			V
V_{IL}		EN logic low voltage				0.4	V
I_{EN}	EN Pull-Down Current	V_{EN} = 5.5V			0.1		uA
e_n	Output Voltage Noise	f = 10Hz to 100kHz,V_{OUT}=2.8V,I_{OUT}=1mA			70		μVRMS
T_{SD}	Thermal shutdown threshold	Shutdown, temperature increasing	I_{OUT}=1mA		175		℃
		Reset, temperature decreasing			145		
R_{DISCH}	Output Discharge Resistance	V_{EN}≤0.2V,V_{IN}=5V （only A version）			100		Ω
t_{ON}	Turn-On Time	From assertion of V_{EN} to V_{OUT} = 90% $V_{OUT(NOM)}$			120		us

Typical Performance Characteristics



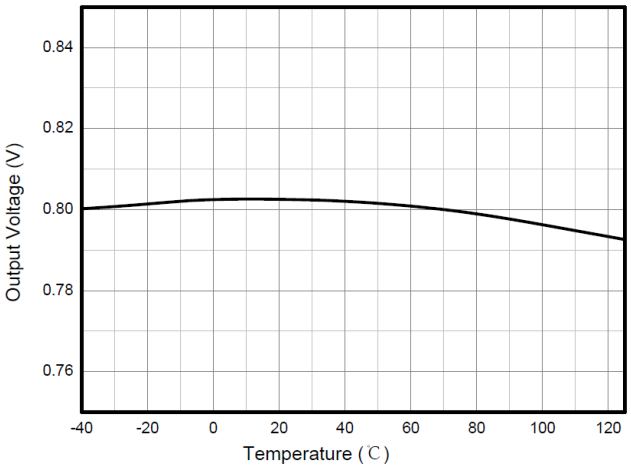
C_{IN} = C_{out} = 1uF， I_{out} = 1mA， V_{out} =0.8V

Figure 2. Output Voltage vs Input Voltage



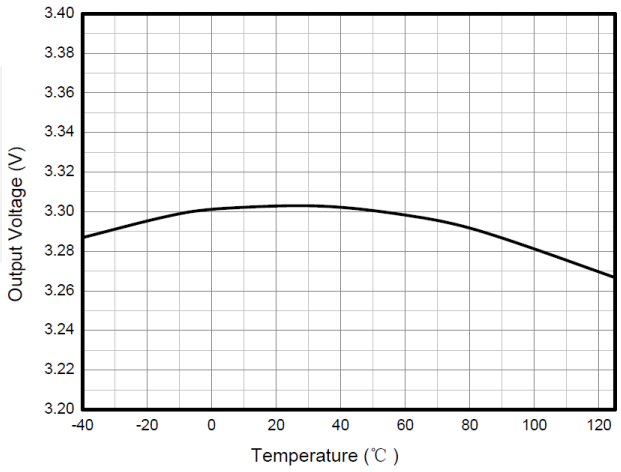
C_{IN} = C_{out} = 1uF， I_{out} = 1mA， V_{out} =3.3V

Figure 3. Output Voltage vs Input Voltage



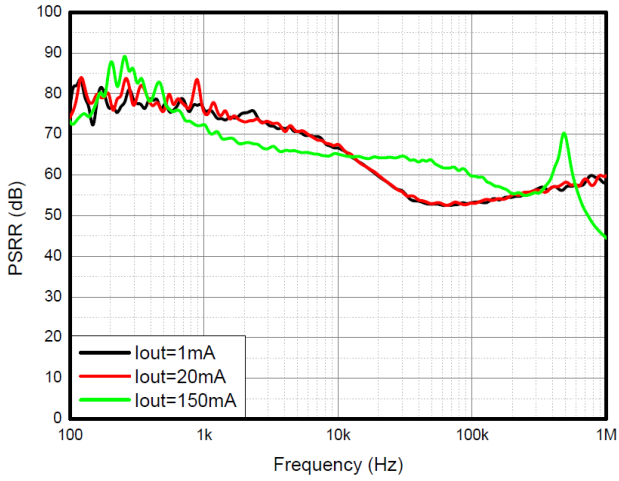
V_{out} = 0.8V， C_{IN} = C_{out}= 1uF， I_{out} = 1mA

Figure 4. Output Voltage vs Temperature



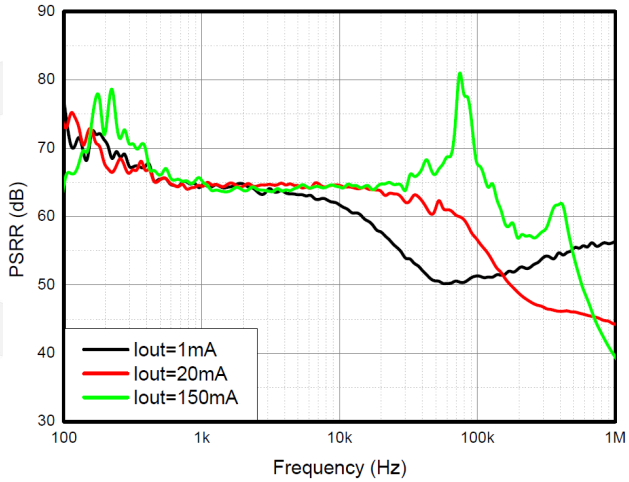
V_{out} = 3.3V， C_{IN} = C_{out} = 1uF， I_{out} = 1mA

Figure 5. Output Voltage vs Temperature



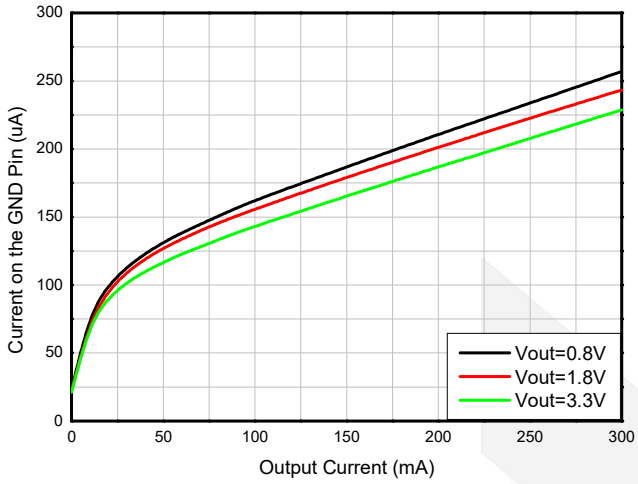
C_{out} = 1uF， V_{IN} = 2.5V+200mVpp， V_{out} = 0.8V

Figure 6. PSRR vs Frequency



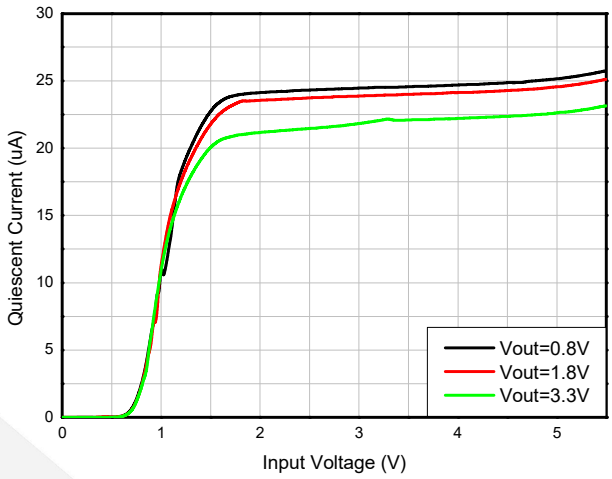
C_{out} = 1uF， V_{IN} = 4.3V+200mVpp， V_{out} = 3.3V

Figure 7. PSRR vs Frequency



C_{out} = 1uF, V_{IN} = V_{out} + 1V or 2.5V whichever is higher

Figure 8. Current on the GND Pin vs Output Current



C_{out} = 1uF , Iout = 0mA

Figure 9. Quiescent Current vs Input Voltage

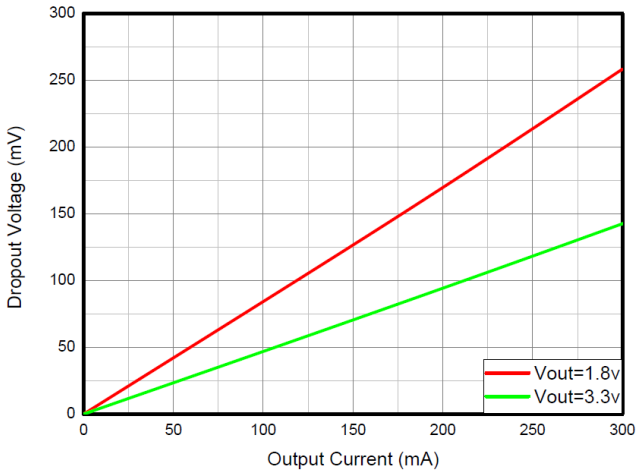
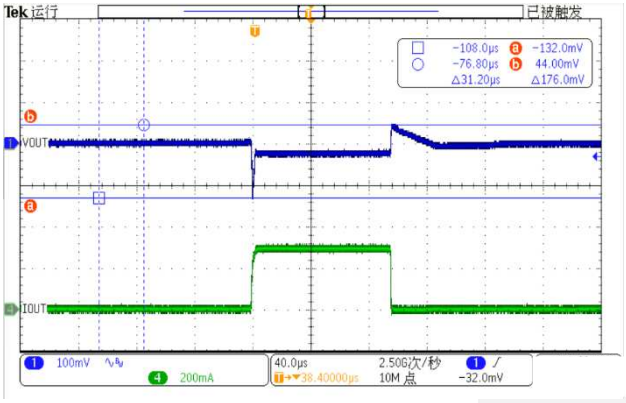
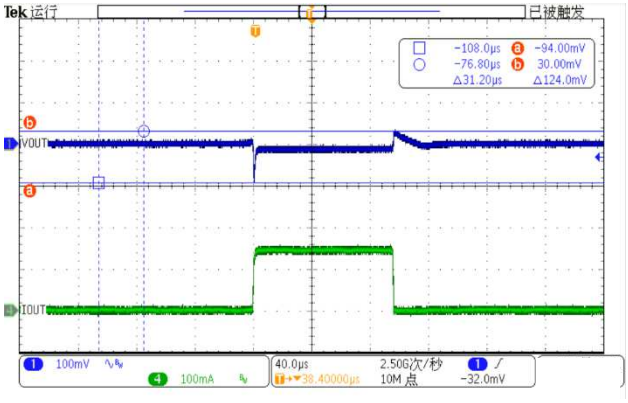


Figure 10. Dropout Voltage vs Output Current



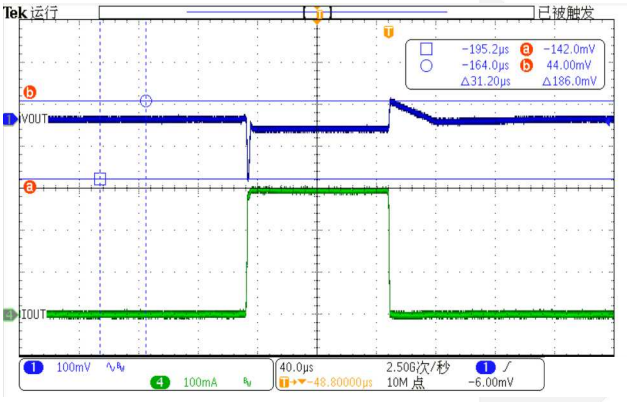
V_{IN} = 2.5V， V_{out} = 0.8V， C_{IN} = C_{out} =1uF

Figure 11. Load Transient Response at Load Step from 1 mA to 300 mA, V_{out} = 0.8 V



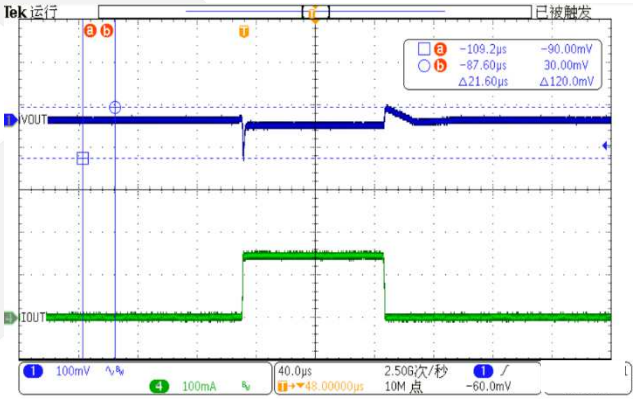
V_{IN} = 2.5V， V_{out} = 0.8V， C_{IN} = C_{out} =1uF

Figure 12. Load Transient Response at Load Step from 1 mA to 150 mA, V_{out} = 0.8 V



V_{IN}=4.3V， V_{out}=3.3V, C_{IN}=C_{out}=1uF

Figure 13. Load Transient Response at Load Step from 1 mA to 300 mA, Vout = 3.3V



V_{IN}=4.3V， V_{out}=3.3V， C_{IN}=C_{out}=1uF

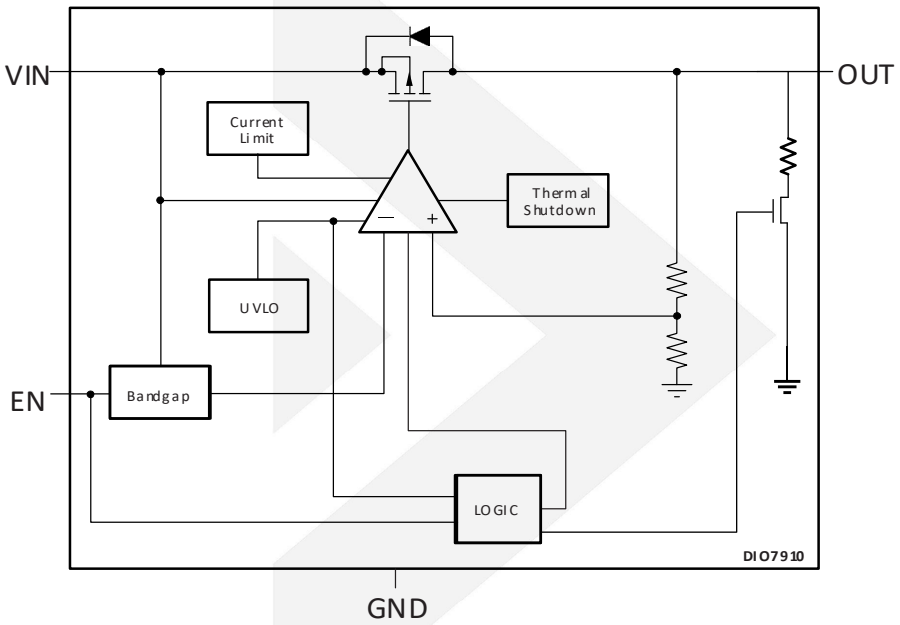
Figure 14. Load Transient Response at Load Step from 1 mA to 150 mA, V_{out} = 3.3 V

Detailed Description

Overview

The DIO7910 series of LDO linear regulators are low quiescent current devices with excellent line and load transient performance. These LDOs are designed for power-sensitive applications. A precision bandgap and error amplifier provides overall 1% accuracy. Low output noise, very high PSRR, and low dropout voltage make this series of devices ideal for most battery-operated handheld equipment. All device versions have integrated thermal shutdown, current limit.

Block Diagram



Internal Current Limit

The DIO7910 internal current limit helps to protect the regulator during fault conditions. During current limit, the output sources a fixed amount of current that is largely independent of the output voltage. In such a case, the output voltage is not regulated, and is VOUT = ICL × RLOAD. The PMOS pass transistor dissipates (VIN – VOUT) × ICL until thermal shutdown is triggered and the device turns off. As the device cools down, it is turned on by the internal thermal shutdown circuit. If the fault condition continues, the device cycles between current limit and thermal shutdown.

The PMOS pass element in the DIO7910 has a built-in body diode that conducts current when the voltage at OUT exceeds the voltage at IN. This current is not limited, so if extended reverse voltage operation is anticipated, external limiting to 5% of the rated output current is recommended.

Shut down

The enable pin (EN) is active high. The device is enabled when voltage at EN pin goes above 1 V. The device is turned off when the EN pin is held at less than 0.4 V. When shutdown capability is not required, EN can be connected to the IN pin.

Dropout Voltage

The DIO7910 uses a PMOS pass transistor to achieve low dropout. When (VIN – VOUT) is less than the dropout voltage (VDO), the PMOS pass device is in the linear region of operation and the input-to-output resistance is the RDS(on) of the PMOS pass element. VDO scales approximately with output current because the PMOS device behaves as a resistor in dropout.

CONTACT US

Dioo is a professional design and sales corporation for high-quality and performance analog semiconductors. The company focuses on industry markets, such as, cell phone, handheld products, laptop, and medical equipment and so on. Dioo's product families include analog signal processing and amplifying, LED drivers and charger IC. Go to http://www.dioo.com for a complete list of Dioo product families.

For additional product information, or full datasheet, please contact with our Sales Department or Representatives.